

-	239	(wir\$3 adj harness and design).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 15:41
-	38	((wiring adj harness and design).ab.) and @ad<20010126	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 15:41
-	107	(wir\$3 adj harness near6 desig\$3).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 15:54
-	67	((wir\$3 adj harness near6 desig\$3).ab.) and @ad<20010126	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/15 10:41
-	1	(virtual near6 wir\$3 adj harness near6 desig\$3).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 15:56
-	1	(virtual near12 wir\$3 adj harness near6 desig\$3).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 15:56
-	6	(virtual and wir\$3 adj harness near6 desig\$3).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 15:56
-	2	5038294.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/15 11:59
-	2	6457165.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/15 17:38

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-	2	((714/727.ccls. and @ad<19980218) and (boundary adj scan adj (register or latch or port))) and (emulat\$3 near3 pad)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 17:38
-	2	((714/727.ccls. and @ad<19980218) and (boundary adj scan adj (register or latch or port))) and (tap near3 link\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 17:34
-	57	((714/727.ccls. and @ad<19980218) and (boundary adj scan adj (register or latch or port))) and (access adj port)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 17:41
-	2	((714/727.ccls. and @ad<19980218) and (boundary adj scan adj (register or latch or port))) and (tap near3 link\$3) and (access adj port)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 17:40
-	0	((714/727.ccls. and @ad<19980218) and (boundary adj scan adj (register or latch or port))) and (tap near3 link\$3) and (core adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 17:40
-	0	((714/727.ccls. and @ad<19980218) and (boundary adj scan adj (register or latch or port))) and (emulat\$3 near3 pad) and (core adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 17:40
-	2	((714/727.ccls. and @ad<19980218) and (boundary adj scan adj (register or latch or port))) and (access adj port) and (core adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/04 17:41
-	2	5546562.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 12:07
-	2	6009256.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 14:35
-	15	(analyte near6 measure\$3 and spectroscop\$3).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 14:42
-	890	(analyte near6 measure\$3).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 14:42
-	97	((analyte near6 measure\$3).ab.) and spectroscop\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 15:33
-	56	(wiring adj harness and design).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/14 15:36